

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 6,982,457 B2  
APPLICATION NO. : 10/781588  
DATED : January 3, 2006  
INVENTOR(S) : Arup Bhattacharyya

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Page 1, Abstract –

Replace “orders of magnitude superior endurance compared to other”  
Insert -- orders of magnitude superior in endurance compared to other --

Col. 3, line 28 –

Replace “Digest, 2002, 00. 98-99; and Huang, L.J. et al., “Carrier”  
Insert --Digest, 2002, pp. 98-99; and Huang, L.J. et al., “Carrier--

Col. 12, line 28 –

Replace “employed by industry, the floating gate devices and the”  
Insert --employed by the industry, the floating gate devices and the--

Col. 16, line 31 –

Replace “incorporate PGA and/or FGPA comprising non-volatile”  
Insert -- incorporate PGA and/or FPGA comprising non-volatile--

Col. 16, line 67 –

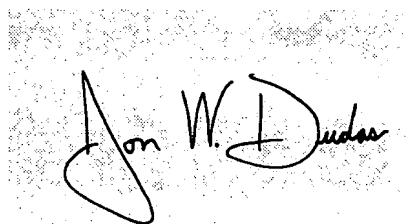
Replace “present invention. One of the ordinary skill in the art will”  
Insert --present invention. One of ordinary skill in the art will--

Col. 18, line 30 –

Replace “17. The device of claim 1 wherein the substratecomprises”  
Insert --17. The device of claim 1 wherein the substrate comprises--

Signed and Sealed this

Twenty-second Day of May, 2007



JON W. DUDAS  
*Director of the United States Patent and Trademark Office*